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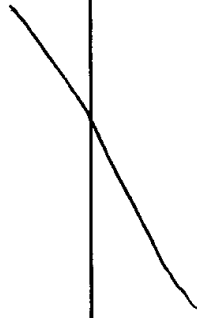
LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Attorney Docket No.</u> SEL 205 DIV	<u>Serial No.</u> Not Assigned
	<u>Applicant</u> Yukio TANAKA	
	<u>Filing Date</u> Herewith	<u>Group</u>

J1036 U.S. PTD  
 10/034797  
 12/28/01

U.S. PATENT DOCUMENTS


*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	5,434,599	07/1995	Hirai et al	345	100	
	5,643,826	07/01/97	Ohtani et al	437	88	10/25/94
	5,703,617	12/1997	Callahan, Jr. et al	345	98	
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	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	JP 7-130652	05/19/95	Semiconductor Energy Laboratory Co., Ltd.			10/29/93
	EP 920 135 A2	06/02/99	Semiconductor Energy Laboratory Co., Ltd.			11/25/98
	JP 11-167373	06/22/99	Semiconductor Energy Laboratory Co., Ltd.			05/11/98
	EP 938 074 A1	08/25/99	Semiconductor Energy Laboratory Co., Ltd.			10/01/98
	JP 20-00036749	02/02/00	Semiconductor Energy Laboratory Co., Ltd.			11/25/98

OTHER PUBLICATIONS

(Including Author, Title, Date, Pertinent Pages)

	1) English Abstract re Japanese Patent Application No. JP 7-130652, published May 19, 1995. 2) English Abstract re Japanese Patent Application No. JP 11-167373, published June 22, 1999. 3) English Abstract re Japanese Patent Application No. JP 20-00036749, published February 2, 2000. 4) U.S. Patent Application No. 09/162,230 (pending) entitled "Semiconductor Display Device and Method of Driving the Same." 5) U.S. Patent Application No. 09/197,766 (pending) entitled "D/A Conversion Circuit and Semiconductor Device."
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EXAMINER: <i>Johnson</i>	DATE CONSIDERED: <i>01/03/03</i>
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